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PPLICATION NO.	FILING DA	TE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/635,825	08/06/20	03 ·	Howard R. Test	TI-33672A	7651	
23494	7590 04	4/12/2004		EXAMINER		
TEXAS INS	TRUMENTS	BARR, MICHAEL E				
P O BOX 655474, M/S 3999 DALLAS, TX 75265				ART UNIT	PAPER NUMBER	
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DATE MAILED: 04/12/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	t reg			
	10/635,825	TEST, HOWARD	R.			
Office Action Summary	Examiner	Art Unit				
·	Michael Barr	1762				
The MAILING DATE of this communication app Period for Reply	ears on the cover shee	t with the correspondence ad	ldress			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply if NO period for reply is specified above, the maximum statutory period who is period for reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	6(a). In no event, however, ma within the statutory minimum o ill apply and will expire SIX (6) cause the application to becom	ay a reply be timely filed  If thirty (30) days will be considered timel  MONTHS from the mailing date of this cone  ABANDONED (35 U.S.C. § 133).	iy. ommunication.			
Status						
1) Responsive to communication(s) filed on	_•					
2a) This action is <b>FINAL</b> . 2b) ⊠ This	action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposition of Claims						
4) ⊠ Claim(s) <u>13-19</u> is/are pending in the application 4a) Of the above claim(s) is/are withdraw 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>13-19</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or	n from consideration.					
Application Papers						
<ul> <li>9) The specification is objected to by the Examiner</li> <li>10) The drawing(s) filed on <u>06 August 2003</u> is/are: Applicant may not request that any objection to the of Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Examiner </li> </ul>	a)⊡ accepted or b)⊠ drawing(s) be held in abe on is required if the draw	eyance. See 37 CFR 1.85(a). ving(s) is objected to. See 37 CF	FR 1.121(d).			
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim for foreign a) ☐ All b) ☐ Some * c) ☐ None of:  1. ☐ Certified copies of the priority documents 2. ☐ Certified copies of the priority documents 3. ☐ Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of	have been received. have been received ity documents have be (PCT Rule 17.2(a)).	in Application No een received in this National	Stage			
Attachment(s)		•				
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftsperson's Patent Drawing Review (PTO-948)</li> <li>Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 8/6/03.</li> </ol>	Paper	ew Summary (PTO-413) No(s)/Mail Date of Informal Patent Application (PTC	D-152)			

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#### **DETAILED ACTION**

# Preliminary Amendment

1. The examiner acknowledges the cancellation of Claims 1-12 and the addition of Claims 13-19. Claims 13-19 are pending.

## **Drawings**

2. Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance. It appears that the applicant's submission of 8/6/2003 intended to amend the drawings to reflect that Fig. 1 is prior art. However, no such amended drawings could be located in the application papers. Therefore, resubmission and/or correction of the drawings, as indicated above, is required.

### **Double Patenting**

3. The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970); and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

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Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 13-19 are rejected under the judicially created doctrine of obviousness-type 4. double patenting as being unpatentable over claims 1, 8, and 11 of U.S. Patent No. 6,616,967 by Test in view of JP 2001-107254 by Honma et al. ("Honma"). Claims 1, 8, and 11 of Test teach the claimed limitations of Claims 13-19 of the present application, except that the substrate is a semiconductor wafer. Claim 1 of Test teaches that the substrate is a copper patterned integrated circuit, but does not indicate that the circuit is part of a semiconductor wafer. However, such copper patterns and circuitry are conventionally used on semiconductor wafer surfaces, as shown by Honma. Honma teaches electroless nickel plating copper containing aluminum input-output terminals of semiconductor wafer circuitry by immersion into an electroless nickel bath (Para. 0029-0030). It would have been obvious to one skilled in the art to use a semiconductor wafer having the copper circuitry thereon, as the substrate in the process of Claims 1, 8, and 11 of Test, with the expectation of providing the desired electroless nickel plating on the copper circuitry, as semiconductor wafers are conventional substrates for such circuitry to be nickel plated, as shown by Honma. Such a modification to Claims 1, 8, and 11 of Test would have obvious to one of ordinary skill in the art and thus Claims 13-19 of the present application are obvious variants to Claims 1, 8, and 11 of Test.

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#### Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Barr whose telephone number is 571-272-1414. The examiner can normally be reached on Monday-Thursday 6:00 am-3:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Shrive Beck can be reached on 571-272-1415. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <a href="http://pair-direct.uspto.gov">http://pair-direct.uspto.gov</a>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Michael Barr Primary Examiner Art Unit 1762

MB April 1, 2004

OMO)